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DANIEL D. MICHAEL	(Depositor's name)
<i>[Signature]</i>	(Signature)
10 June 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/067,206	02/05/2002	Franciscus Gerardus Maria De Jong	NL010324	7031

TITLE OF INVENTION: TEST CIRCUIT AND TEST METHOD FOR PROTECTING AN IC AGAINST DAMAGE FROM ACTIVATION OF TOO MANY CURRENT DRAWING CIRCUITS AT ONE TIME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/14/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
TANG, MINH NHUT	2829	324-763000

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1. MICHAEL J. JURE
2. _____
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Koninklijke Philips Electronics N.V.

Eindhoven, The Netherlands

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

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